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| LIST OF DOCUMENTS CITED BY APPLICANT(S) (Use several sheets if necessary) | | | | | | | | |
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| Pending U.S. Application No. 10/678,028 (TI-33654), Pendharker et all "A Low No. Variable Gate Control Voltage JFET Device in a BICMOS Process & Methods to I Device" | | | | | | | | |
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| *EXAMINER: Initial if r | eference considered, whether or not citation ance and not considered. Include copy of the | | | | şh | | | |